

Ross Wolin

Everett, WA

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Electrical Engineer specializing in embedded systems firmware, software and hardware design. I have a BSEE from NY's Rensselaer Polytechnic Institute and decades of industry experience.

Provides fully equipped lab and office: offsite projects only.

Clients include: Accomplio, Adeneo Embedded, Allied Signal, Alpha Technologies, Apricorn, BSquare, Burke, Centri Technology, Clausius Technologies, Cortland Line, DBC Avionics, DEC, Discovery Bay Games, Dun&Bradstreet, EG&G, Emulex, E*swing, EZLYNK, FiftyThree, Ford Motor Company, Fugu Ltd, Galactic Industries, IBM, Metron, Metronics Systems, Microdyne, MicroEncoder/Mitutoyo, Microsoft, Mobile Integration Workgroup, Myotronics, Nebula, Optical Technology Devices, Protean Payment, Purigen Biosystems, Radio Thermostat Company of America, Sarcos Robotics, SeaMED/Plexus, Silicon Mechanics, Starwave, Stratos, Sun Microsystems / Oracle, Sunfire, SunStar Timers, Traveling Software/TSI, Ventec Life Systems, Wang Laboratories, WD Machines, XLI, and YTCA.

Firmware/Software services

- Embedded systems firmware design/debug, (C/C++, assembler)
- Raspberry Pi, Arduino, ESP32 based rapid prototyping
- Embedded Linux+Android kernel/device drivers/BSP
- Cross-platform app development (Qt, wxWidgets, C/C++/C#/Python)
- Reverse engineering with IDA Pro, OllyDbg, and other hw+sw tools
- Custom Yocto generated embedded Linux distros/kernels/BSPs
- DSP (Digital Signal Processing)
- OpenGL and 3D graphics/games, game engines (3D physics, collision detection, etc)
- TCP/IP networking

Hardware services

- Rework, including microscope work
- Hardware bringup/debug using scope, logic analyzer, and incircuit emulators
- Hardware design
- Schematic capture (OrCAD, KiCAD)
- FPGA/CPLD circuit design (VHDL)
- PCB layout

Other areas of experience

- ARM7/9/11/Cortex-Mx/Cortex-Ax, MX51/31, OMAP, STM32, SAM3X8E, ColdFire, 68332, 80x86, PPC, DSP, AVR, PIC, 68HC11/12, 8051, Z80, EZUSB, TI254x
- USB, 1394/Firewire, RS232, SPI, SCI, I2C, A/D, LCD, TCP/IP, IrDA, CAN, ICE, ICD, BDM
- Git, Mercurial, SVN, Visual Source Safe, SLM, RCS/Picasso, Github, Bitbucket, SourceForge
- Python, Perl, Bash, Make and C Shell Scripting
- Object Oriented Design, STL
- Xilinx, Altera, VHDL

Interests: Avid XC/telemark/AT backcountry skier, mountain biker, kayaker, snowboarder, home brewer, tinkerer